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Number	Document ID	Issue Date	Pages	Title	Current US	Current EP	Retrieval	Owner	P	C	D	E	F	G	H
42	P	US 5493454 A	19960220	Write precompensation optimization in a PRML	360/45	360/46; 360/53	C	Liporovich, Pablo A. et al.	F	F	F	F	F	F	F
43	P	US 5459757 A	19951017	Timing and gain control circuit for a PRML read	375/376	360/46; 360/51	C	Minuhin, Vadim B. et al.	F	F	F	F	F	F	F
44	P	US 5422760 A	19950606	Disk drive method using zoned data recording and	360/46	360/51; 360/65	C	Abbott, William L. et al.	F	F	F	F	F	F	F
45	P	US 5402274 A	19950328	Data separator and signal processing circuit with an	360/51	360/53	C	Miyazawa, Shyoichi et al.	F	F	F	F	F	F	F
46	P	US 5381359 A	19950110	Adaptation and training of digital finite impulse	708/322	360/40	C	Abbott, William L. et al.	F	F	C	F	C	F	F
47	P	US 5375145 A	19941220	Multi-mode gain control loop for PRML class IV sampling	375/345	375/290; 455/234	C	Abbott, William L. et al.	F	F	F	C	F	F	F
48	P	US 5369376 A	19941129	Programmable phase locked loop circuit and method of	331/8	331/16; 331/17	C	Leblebicigoglu, Hakan	F	F	C	F	C	F	F
49	P	US 5345342 A	19940906	Disk drive using PRML synchronous sampling data	360/46	360/77, 08	C	Abbott, William L. et al.	F	F	F	C	F	F	F
50	P	US 5341387 A	19940823	Viterbi detector having adjustable detection	714/788	375/341; 375/346	C	Nguyen, Hung C.	F	F	F	F	F	F	F
51	P	US 5341249 A	19940823	Disk drive using PRML class IV sampling data detection	360/46	360/39; 360/51	C	Abbott, William L. et al.	F	F	F	F	F	F	F
52	P	US 5331479 A	19940719	Capacitor assisted low voltage write circuit	360/68	360/46; 360/66	C	Wadson, Timothy A.	F	F	F	F	F	F	F
53	P	US 5321559 A	19940614	Asynchronous peak detection of information embedded	360/46	360/51; 360/65	C	Nguyen, Hung C. et al.	F	F	F	F	F	F	F
54	P	US 5278702 A	19940111	Data synchronizer with symmetric window generation	360/51	360/46; 375/376	C	Wilson, Ronald E. et al.	F	F	F	C	F	F	F
55	P	US 5258933 A	19931102	Timing control for PRML class IV sampling data	708/3	360/46; 360/51	C	Johnson, Kenneth E. et al.	F	F	C	F	C	F	F
56	P	US 5231545 A	19930727	Fault tolerant RLL data sector address mark decoder	360/49	341/59; 360/40	C	Gold, Clifford M.	F	F	F	F	F	F	F
57	P	US 5212601 A	19930518	Disk drive data synchronizer with window shift synthesis	360/51	360/44	C	Wilson, Ronald E.	F	F	C	F	C	F	F
58	P	US 5187615 A	19930216	Data separator and signal processing circuit	360/46	327/160; 360/51	C	Miyazawa, Shyoichi et al.	F	F	F	C	F	F	F
59	P	US 5025327 A	19910618	Magnetic disk write precompensation method and	360/45	360/27	C	Stamness, Jesse I. et al.	F	F	F	C	F	F	F
60	P	US 4989219 A	19910129	Midlevel carrier modulation and demodulation techniques	375/286	329/363; 332/115	C	Gerdos, Richard C. et al.	F	F	F	F	F	F	F
61	P	US 4964107 A	19901016	Relative timing precompensation of	368/120	360/51	C	Galbraith, Richard L. et al.	F	F	F	F	F	F	F
62	P	US 4878028 A	19891031	Technique for generating precompensation delay for	327/276	327/101; 327/262	C	Wang, Yun-Che et al.	F	F	F	F	F	F	F
63	P	US 4809088 A	19890229	Integrated system for implementation of read	360/31	360/45; 360/51	C	Lofgren, Karl M. J. et al.	F	F	F	F	F	F	F
64	P	US 4544964 A	19851001	Strobe for read/write chain	360/51	C	Burns, Kenneth R. et al.	F	F	F	F	C	F	F	
65	P	US 4281356 A	19810728	Magnetic disk memory	360/45	360/51	C	Sousa, Frank E.	F	F	F	C	F	F	F

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